

Amendments to the Claims

This listing of claims will replace all prior versions, and listing of claims in the application.

Listing of Claims:

Claims 1-26 (Canceled)

Claim 27 (Currently Amended): ~~[[The]]~~ A level shift circuit of claim 26 comprising:

a first transistor circuit which electrically connects a first node to a first power supply line when a second node is at a second power supply potential, and which does not electrically connect said first node to said first power supply line when said second node is at a first power supply potential;

a second transistor circuit which electrically connects said second node to said first power supply line when said first node is at said second power supply potential, and which does not electrically connect said second node to said first power supply line when said first node is at said first power supply potential;

a third transistor circuit which electrically connects said first node to a second power supply line when an input signal is at a first input potential, and which does not electrically connect said first node to said second power supply line when said input signal is at a second input potential;

a fourth transistor circuit which electrically connects said second node to said second power supply line when said input signal is at a second input potential, and which does not electrically connect said second node to said second power supply line when said input signal is at said first input potential; and

a fifth transistor circuit which switches a value of an inflow current or emission current of said second node or said first node according to a control signal, when said second node or said first node is electrically connected to both of said first power supply line and said second power supply line,

wherein said first transistor circuit comprises a first conductive type first transistor one end of which is connected to said first power supply line and a control terminal of which is connected to said second node, and a first conductive type second transistor one end of which is connected to an other end of said first transistor, an other end of which is connected to said first node, and a control terminal of which has said input signal provided thereto,

said second transistor circuit comprises a first conductive type third transistor one end of which is connected to said first power supply line and a control terminal of which is connected to said first node, and a first conductive type fourth transistor one end of which is connected to an other end of said third transistor, an other end of which is connected to said second node, and a control terminal of which has an inverted value of said input signal provided thereto,

said third transistor circuit comprises a second conductive type fifth transistor one end of which is connected to said second power supply line, an other end of which is connected to said first node, and a control terminal of which has said input signal provided thereto, and

said fourth transistor circuit comprises a second conductive type sixth transistor one end of which is connected to said second power supply line, an other end of which is connected to said second node, and a control terminal of which has the inverted

value of said input signal provided thereto,

wherein said fifth transistor circuit comprises[[:]]

a second conductive type seventh transistor one end of which is connected to said second power supply line and a control terminal of which has said control signal provided thereto[[:;]], and

a second conductive type eighth transistor one end of which is connected to an other end of said seventh transistor, an other end of which is connected to said second node, and a control terminal of which has an inverted value of said input signal provided thereto.